

WB32F10x Program Download Guide

Westberry Technology (ChangZhou) Corp., Itd



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1 Use The JFLASH Software To Programming

1.1 Configuration J-Link

Step 01, Create a new folder named WB in j-link software installation directory

Step 02, Copy Programming Algorithm (WB32F10x_256.FLM) to folder: <u>C:\Program Files(x86)\SEGGER\JLink V614b\Devices\WB\WB32F10x</u>

Step 03, Add the following information to this file

C:\Program Files (x86)\SEGGER\JLink V614b\JLinkDevices.xml and save it.

<Device>

<ChipInfo Vendor="WB" Name="WB32F10x" Core="JLINK_CORE_CORTEX_M3" WorkRAMAddr="0x20000000" WorkRAMSize="0x1000" /> <FlashBankInfo Name="Internal Flash" BaseAddr="0x08000000" MaxSize="0x40000" Loader="Devices\WB\WB32F10x\WB32F10x_256.FLM" LoaderType="FLASH_ALGO_TYPE_CMSIS" AlwaysPresent="1" />

</Device>



1.2 Use J-Flash to programming

Step 01, Open the J-Flash software.



Step 02, Create New Project and Select WB32F10x as target device

File	Edit View Target Options Window	Help			
1	Open data file Myrge data file	Ctrl+O			
C	Save data file Save data file as2 New project	Ctrl+S			
	Save project				
	Save project as Close project		(Create New Project	×
	Save Flasher config file Save Flasher data file			Target Device	
	Download config & data file to Flasher		~ ~	Little endian 💌	3
	Download serial number file to Flasher				
	Export setup file			Target Interface	Speed (kHz) 4000
	Recent Files	>	<i></i>		
	Recent Projects	>			ОК
	Exit	Alt+F4			

Manufacturer WB		1		
Manufacturer	Device	Core	Flash size	BAM size
WB	WB32F10x	Cortex-M3	256 KB	36 KB
		2		
			З	Cancel



Step 03, Click OK and you will see the information in the lower right picture

Create New Project		×
Target Device		
WB WB32F10x		
Little endian 💌		
Target Interface	Speed (kHz)	
SWD 💌	4000	•
L		ОК

Project - new	w p 🗖 🗖 💌
Name	Value
Host connection	USB [Device 0]
Target interface	SWD
Init SWD speed	4000 kHz
SWD speed	4000 kHz
МСО	WB WB32F10x
Core	Cortex-M3
Endian	Little
Check core ID	No
Use target RAM	36 KB @ 0x20000000
Flash memory	Internal bank 0
Base address	0x8000000
Flash size	256 KB

Step 04, Click on the File->Open data file... and select the HEX file





Step 05, Connect the device and then click **Target->manual Program & Venify** to start programmed target chip

SEGGER J-Flas	h V6.32 - [new project *]													
File Edit View	Target Options Wi	indow Help													
Project - new	Connect Disconnect 1			es	ktop	o\WB	332F	10x_9	StdPe	eriph	_Lib_	V0.1	.9\Pr	oject	t\WB
Host connection	Test		>		2	3	×1 4	×2 5	× <u>4</u> 6	7	8	9	A	B	С
Target interface Init SWD speed SWD speed	Production Prog Manual Program	ratiming	F7	5	00	20 Secu	59 re Ch	01	00	08	61	01 1	00 00	Ø8 Ø8	63 00
MCU Core	WB WB32F10x 2 Cortex-M3	8000030	6D	¢.	l	Jnse	cure	Chip	0			10 11	00 00	00 08	6B 71
Endian Check core ID Use target RAM	Little No 36 KB @ 0x20000000	8000040	73 73 73	i	E	Chec Erase	k Bla e Sec	ank tors			F2 F3	11	00 00	08 08	73 73 73
Flash memory Base address	Internal bank 0 0x8000000	8000070 8000080	73 73	e E		Frase	e Chi	р			F4	11 11	00 00	Ø8 Ø8	73 73
Flash size	256 KB	8000090 80000A0	73 73		F	Prog	ram	& Ve	erify		F6		00 00 00	08 08 09	73 73
		80000D0	73 73	(١	Verify Read	y Ibac	k	3		F8 >	1 1 0	00 02	08 F8	73 73 00
		80000E0 80000F0	0A 01	f 1.	9	Start	Арр	licati	on		F9	.4 JE	AA Ba	F1 E8	01 0F
		8000100 8000110 8000120	01 0C	0F 03 C1	18 00 pd	BF 00 100	FB 00	1A 23	43 00	FØ 24 DD	01 00 20	Ø3 25 C1	18 00 19	47 26 DD	FC 10 00

Step 06, Target Programmed and verified successfully

J-Flash V6.32

Target programmed and verified successfully (CRC = 0x27024487) - Completed after 0.130 sec

确定

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Revision History

Revision	Date	Description
1.2	2019/08/05	Initial Release



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